CLAIMS

1. An electronic microcircuit module (1) including a substrate (2), at least one contact area (4) on a first face (3) of this substrate, a second face (6) of this substrate capable of accommodating an integrated circuit (10), characterised in that it includes a parallelepiped shape, a first adhesive means (8) to retain a first face of a mask (7) in position against the second face of the substrate, the mask delimiting the parallelepiped being perforated to form a window around the integrated circuit and a second adhesive means dispensed on a second face of the mask.

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- 2. The module according to claim 1, characterised in that the mask is made from a material identical to that of a card provided to receive the module, for example from a polymer of polyester or polyvinyl chloride type.
- 3. The module according to either of claims 1 or 2, characterised in that the mask has a thickness, defined with regard to the second face of the substrate on which it is mounted, greater than the height of the integrated circuit.
- 4. The module according to any of claims 1 to 3, characterised in that the first adhesive means enables the integrated circuit to be retained on the substrate.
- 5. A method for conditioning an electronic microcircuit module (1), characterised in that it includes the following stages consisting of:
 - creating a contact area (4) on a first face (3) of a substrate tape (2),
- arranging a first adhesive means (8) between a second face of the substrate and a first face of a mask tape (7), to keep the mask in position against the second face,
- perforating the mask tape so that a mask window is facing the contact area,
- arranging a second adhesive means on the second face of the mask, 30 and
 - separating the individual module in the form of a parallelepiped.
 - 6. The method according to claim 5, characterised in that it includes an additional stage consisting of:
- choosing a mask in a material identical to that of a card on which the module is to be mounted.

- 7. The method according to either of claims 5 or 6, characterised in that the mask has the form of a tape including several windows which are laminated on a support including several contact area before separation into individual units.
- 8. The method according to any of claims 5 to 7, characterised in that the stage consisting in retaining the mask in position against the second face of the substrate includes an operation consisting of:

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- laminating the first adhesive means on this second face of the substrate.
- 9. The method according to any of claims 5 to 8, characterised in that the stage consisting in arranging an adhesive means on the mask includes an operation consisting of:
 - depositing the adhesive means on the mask, and then
- perforating this mask before laminating it against the second face of
 the substrate.
 - 10. The method according to any of claims 5 to 9, characterised in that it includes a subsequent stage consisting in gluing an integrated circuit to the second face of the substrate, on the first adhesive means.
 - 11. The method according to any of claims 5 to 10, characterised in that it includes a stage consisting of:
 - gluing the mask equipped with an electronic circuit to the bottom of a card recess.
 - 12. The method according to claim 11, characterised in that the stage consisting in gluing the mask to the recess includes an operation consisting of:
 - depositing cyanoacrylate glue between the mask and the bottom of the recess.
 - 13. The method according to claim 11, characterised in that the stage consisting in gluing the mask into the recess includes an operation consisting of:
 - soldering by emission of ultrasound waves.